

A METHOD AND SYSTEM FOR AUTOMATICALLY CALIBRATING INTRA-
CYCLE TIMING RELATIONSHIPS FOR SAMPLING SIGNALS FOR AN
INTEGRATED CIRCUIT DEVICE

5 ABSTRACT OF THE DISCLOSURE

A method for automatically calibrating intra-cycle timing relationships between command signals, data signals, and sampling signals for an integrated circuit device. The method includes generating command signals for accessing an integrated circuit component, accessing data signals for conveying data for the integrated circuit component, and accessing
10 sampling signals for controlling the sampling of the data signals. A phase relationship between the command signals, the data signals, and the sampling signals is automatically adjusted to calibrate operation of the integrated circuit device.